

Build an analogue to digital converter

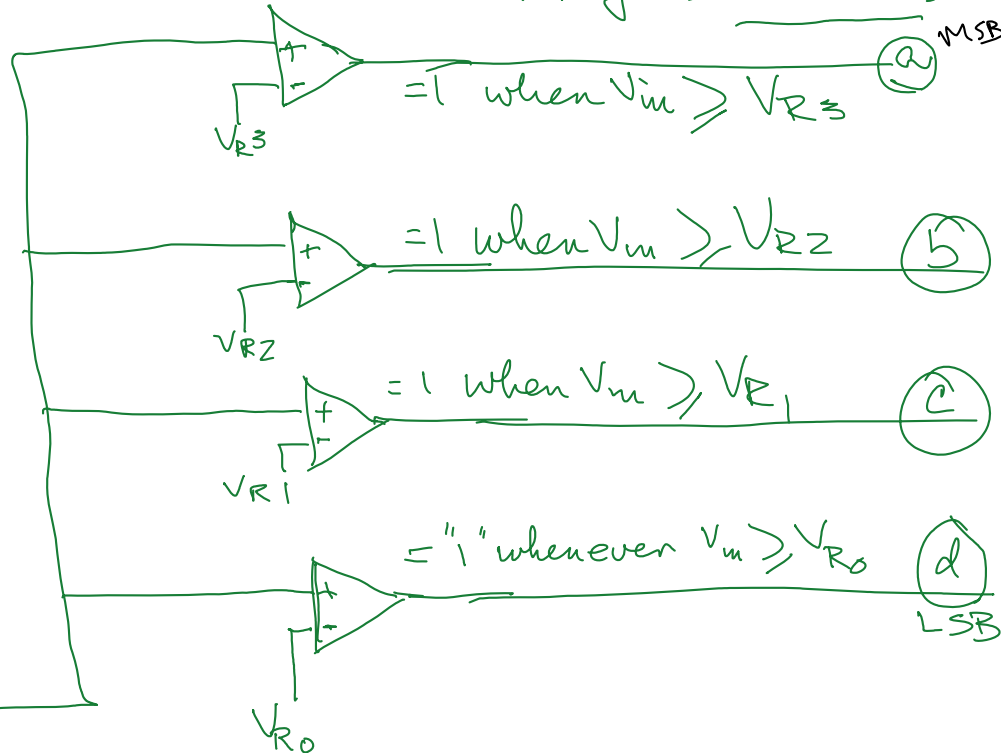
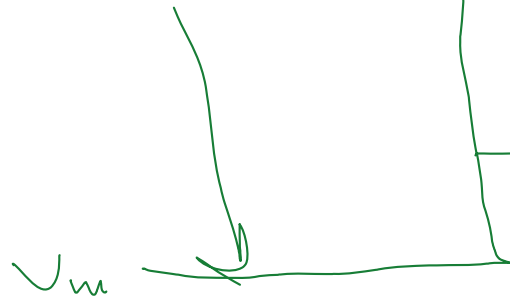
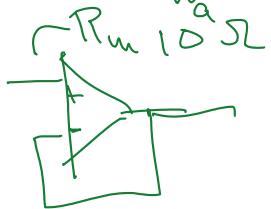
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$V_{in} \Rightarrow$ if higher than some $V \rightarrow$ go from 0000 to 0001

Compare V_{in} with V , and if $V_{in} \geq V$, Set output to "1"

Power Supply $\Rightarrow +V = 5V, -V = 0V \Rightarrow$ Automatic TTL

Could put
FET buffer Amp



Reference Voltages

How much current
flows into an
op amp

(1) Adjust V_- to make $= V_+$

(2) no current flows
into the op amp